1. Late allocation and early release of physical registers Monreal, T., Vinals, V.; Gonzalez, J.; Gonzalez, A.; Valero, M.; Computers, IEEE Transactions on Volume 53, Issue 10, Oct. 2004 Page(s):1244 - 1259 Digital Object identifier 10.1109/TC.2004.79 2. Use-based register caching with decoupled indexing Butts, J.A., Sohi, G.S.; Computer Architecture, 2004. Proceedings, 31st Annual International Symposium on 19-23 June 2004 Page(s):302 - 313 Digital Object Identifier 10.1109/ISCA.2004.1310783 AbstractPlus | Full Text: PDF(327 KB) HEEF CNF Rights and Permissions Compiler-based register name adjustment for low-power embedded processors Petrov, P.; Orailoglu, A.; Computer Aided Design, 2003. ICCAD-2003. International Conference on 9-13 Nov. 2003 Page(s):523 - 527 Digital Object Identifier 10.1109/ICCAD.2003.1257861 AbstractPlus | Full Text: PDE(530 KB) : EEE CAF Rights and Permissions 4. Stream register files with indexed access Jayasena, N.; Erez, M.; Ahn, J.H.; Dally, W.J.; High Performance Computer Architecture, 2004. HPCA-10, Proceedings, 10th International Symposium on 14-18 Feb. 2004 Page(s):60 - 72 Digital Object Identifier 10.1109/HPCA.2004.10007 AbstractPlus | Full Text: PDF(432 KB) #6.68 ONF Rights and Permissions 5. Partitioning variables across register windows to reduce spill code in a low-power processor Ravindran, R.A.; Senger, R.M.; Marsman, E.D.; Dasika, G.S.; Guthaus, M.R.; Mahlke, S.A.; Brown, R.B.; Computers, IEEE Transactions on Volume 54, Issue B. Aug. 2005 Page(s):998 - 1012 Digital Object Identifier 10.1109/TC.2005.132 AbstractPlus I Full Text: PDF(1576 KB) IEEE UNI. Rights and Permissions 6 A speculative control scheme for an energy-efficient banked register file Tseng, J.H.: Asanovic, K.: Computers, IEEE Transactions on Volume 54. Issue 6. Jun 2005 Page(s):741 - 751 Digital Object Identifier 10.1109/TC.2005.88 AbstractPlus | Full Text: PDF (1272 KB) | IFEE INC Rights and Permissions 7. Using a resource-limited instruction scheduler to evaluate the iHARP processor Steven, F.L.; Steven, G.B.; Wang, L.; Computers and Digital Techniques, IEE Proceedings -Volume 142, Issue 1, Jan. 1995 Page(s):23 - 31 AbstractPlus | Full Text: PDF (656 KB) HET JIN:

	8.	Updating strategy based architecture for reference picture management in H.264/AVC Kal Luc; Dorgolds Li Li Langhao Many, Mang Ziang; ASIC, 2007. ASICON 37. 7th International Cordenance on 22-25 Oct. 2007 Page(s):217 - 2200 Jordan Cibect Identific 10.1109/ICASIC.2007.4415606
		AbstractPlus Full Text: POF(127 KB) # EE CAF Bignss and Permissions
	9.	pFilter: global information littering and dissemination using structured overlay networks Churdeag Tang, Zhidhen Xu. Established Companing Systems, 2003. FTDCS 2608. Proceedings. The Nath IEEE Workshop on Future, Trevids of 28-30 May 2003 Pagels (24-3) Option Capet Indentific 10.1 104FTDCS 2003.1204290
		AbstractPlue Full Text: PDE(295 KB) # EST CAST Bighte and Permissions
	10.	Two types of sound tool for editing speech signal: sound cutter and symbolic sound editor Sagisaka, T. Munakata, T.; Multimedis Computing and Systems, 1993. IEEE International Conference on Volume 2, 7-11 June 1999 Page(s): 1020 - 1021 vol.22 Digital Ceipert Identifier 10.1109 AMMCS.1999.778642 Abstract/Else Full Text: PDE/156 KB) IEEE C.NF
		Fights and Permessions
	11.	Code size reduction in heterogeneous-connectivity-based DSPs using instruction set extensions Biswae, P.; Dutt. N.D.; Computers, IEE (Transactions on Volume S4, Issue 1.0, Oct. 2006 Page(s):1216 - 1226 Digital Object Identifier 1.0,1109/TC 2005.157 John Code Code Code Code Code Code Code Code
		Bigliss and Permessions
	12	Dynamic resisting of superscalar datapeth components for energy efficiency Procriency D. Kuck, G. Gloson K. Computers, IEEE Transactions on Volume 65, Issaed, F. Peb. 2009 Rage(s):199 - 213 Updat Oliçat Indentific 10.1109TC.2008.23
		AbstractPlus Full Text: PDE (1568 KB) HE EE JPH. Rights, and Permissions
	13.	News: new partis in hosted data? Goth, G; Destituted Syntems, Online, IEEE, Volume 7, Issue 5, May 2006 Uplat Object Indentifie 10.1109/MDSO 2006.32
		AbstractPlus Full Text: PDE(73 KB) ## CE Uhs. Bights and Permissions
	14	Distributed data cache designs for clustered VLIW processors Giber, E.; Sanchez, J.; Gonzalez, A.; Conputans, IEEE, Teppeaducie, pp. 1 Volume SA, Ipseu, SJ, Oct. 2005 Pape(s):1227 - 1241 Digital Checis Identific 10.11091
		AbstractPus Full Text PDF (1632 KB) RBR NR. Robbs and Sammericae